

ABSTRACT

In a horizontal driving circuit 17 using a clock driving method in a point-sequence driving method active-matrix type display device, clocks DCK and DCKX having the same period and having a smaller duty ratio than horizontal clocks HCK and HCKX are generated at a clock generating circuit 18. These clocks DCK and DCKX are sampled by switches 22-1 to 22-4 of a clock sampling switch group 22. Then, by driving switches 23-1 to 23-4 of a sampling switch group 23 by these sampling pulses Vh1 to Vh4, a video signal VDO is sampled by perfectly non-overlapping sampling. As a result, occurrence of streaks on a display screen caused by overlapping sampling can be suppressed, and a ghost margin can be raised.

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